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ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10041609	FILING DATE 01/10/2002	CLASS 438	SUBCLASS /	GAU 2818	EXAMINER K. J. K. K.
<b>**APPLICANTS:</b> Matsumura Hideki; Izumi Akira; Masuda Atsushi; Nashimoto Yasunobu; Miyoshi Yosuke; Nomura Shuji; Sakurai Kazuo; Aoshima Shouichi;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/102.665 06/23/1998 PAT 6.349.669 WHICH IS A DIV OF 08/924.304 09/05/1997 PAT 6.069.094					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 8-257675 09/06/1996					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 971003B	
<b>TITLE :</b> Method and apparatus for depositing a thin film, and semiconductor device having a semiconductor-insulator junction					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>
Amount Due	Date Paid		Sheets Drwg.   Figs.Drwg.   Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE	Application Examiner
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